

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Richard R. Hall et al.

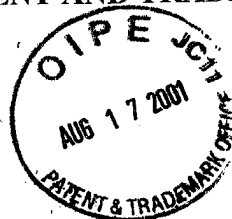
Serial No.: 09/884,778

Filing Date: 06/19/2001

Art Unit: 2841

Dkt. No.: END920000187US1

Title: METHOD AND APPARATUS TO ESTABLISH CIRCUIT LAYERS
INTERCONNECTIONS



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CORRECTION OF FILING RECEIPT

Sir:

Applicants respectfully request correction of the filing receipt for this case as indicated on the attached copy of the filing receipt.

Consideration of the foregoing in relation to this patent application is respectfully requested.

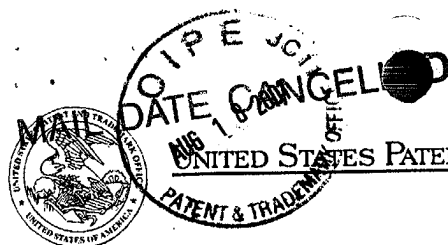
Respectfully submitted,


John A. Merecki
Reg. No. 35,812

Date: August 14, 2001

I HEREBY CERTIFY THAT THE CORRESPONDENCE TO WHICH THIS STATEMENT IS AFFIXED IS BEING DEPOSITED WITH THE UNITED STATES POSTAL SERVICE, POSTAGE PAID, AS FIRST CLASS MAIL, IN AN ENVELOPE ADDRESSED TO ASSISTANT COMMISSIONER FOR PATENTS, WASHINGTON, D.C. 20231 ON August 14, 2001


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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
09/884,778	06/19/2001	2841	1086	END920000187US1	3	32	5

CONFIRMATION NO. 2338

5409
ARLEN L. OLSEN
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LATHAM, NY 12110

FILING RECEIPT



OC000000006411916

Date Mailed: 08/10/2001

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Assignment For Published Patent Application

International Business Machines Corporation, Armonk, NY;

Domestic Priority data as claimed by applicant

Foreign Applications

If Required, Foreign Filing License Granted 08/10/2001

Projected Publication Date: 12/19/2002

Non-Publication Request: No

Early Publication Request: No

Title

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Title Sheet Read:

Method and apparatus to establish circuit layers *interconnections*

Preliminary Class

361

Data entry by : MOGUSS, ZENEBEWORK

Team : OIPE

Date: 08/10/2001



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Title 37, Code of Federal Regulations, 5.11 & 5.15

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